

Claims

[c1] What is claimed is:

1.A liquid crystal display comprising:

- a plurality of signal lines;
- a plurality of scanning lines; and
- a plurality of pixels, each pixel comprising
- a liquid crystal cell having a pixel electrode and a storage capacitor, and
- a switching transistor comprising a gate electrode connected to a scanning line,
- a drain electrode connected to a signal line, and a source electrode connected to the pixel electrode;
- wherein an area of an overlapping region of the gate electrode and the source electrode is increased by increasing a distance between an input end of the scanning line corresponding to the overlapping region and the pixel corresponding to the overlapping region.

[c2] 2.The liquid crystal display of claim 1 wherein the gate electrode comprises a first block located within the overlapping region, and an area of the first block is increased by increasing the distance.

[c3] 3.The liquid crystal display of claim 1 wherein the source electrode comprises a second block located within the overlapping region, and an area of the second block is increased by increasing the distance.

[c4] 4.The liquid crystal display of claim 2 wherein the gate electrode further comprises a pair of protective structures located on both sides of the first block for preventing the first block from being separated from the gate electrode.

[c5] 5.A liquid crystal display comprising:

- a scanning line connected to a scanning line control circuit;
- a first region of the scanning line comprising at least a first transistor, which has a first gate electrode connected to the scanning line, a first drain electrode connected to a first signal line, and a first source electrode connected to a first pixel electrode, wherein a first overlapping region exists between the first gate electrode and the first source electrode; and
- a second region adjacent to the first region of the scanning line comprising at

least a second transistor, which has a second gate electrode connected to the scanning line, a second drain electrode connected to a second signal line, and a second source electrode connected to a second pixel electrode, wherein a second overlapping region exists between the second gate electrode and the second source electrode;

wherein the first region is located between the scanning line control circuit and the second region, and an area of the second overlapping region is greater than that of the first overlapping region by a predetermined value.

[c6] 6.The liquid crystal display of claim 5 wherein the first gate electrode comprises a first block located within the first overlapping region, and the second gate electrode comprises a second block located within the second overlapping region, and an area of the second block is greater than that of the first block by the predetermined value.

[c7] 7.The liquid crystal display of claim 6 wherein the first gate electrode further comprises a pair of protective structures located on both sides of the first block for preventing the first block from being separated from the first gate electrode.

[c8] 8.The liquid crystal display of claim 5 wherein the first source electrode comprises a third block located within the first overlapping region, and the second source electrode comprises a fourth block located within the second overlapping region, and an area of the fourth block is greater than an area of the third block by the predetermined value.